BIO-DATA

Name: **Dr. SachchidaNand Shukla**

Designation: **Professor** and **Ex-Head**
Department of Physics and Electronics
Dr. Ram Manohar Lohia Avadh University,
Faizabad - 224001, U.P., INDIA

Father's Name: Late Shri Ram Chandra Shukla

Bate of Birth January 20, 1967

E-Mail: sachida_shukla@yahoo.co.in, sachida.shukla@gmail.com

Address (Residence): 07, Avadhpuri Colony, Amaniganj, Faizabad -224001, U.P., INDIA
Phone- +91-5278-240651, Mob. +91 9415188149

ACADEMIC VISITS (ABROAD)

1. Asian Regional Conference and Workshop on Advanced Reconfigurable Instrumentation for Scientific Applications, Universiti Kebangsaan Malaysia, Bangi, Selangor, **MALAYSIA,** November 14-25, 2016 (organized by ICTP, Italy)
2. IEEE Radio and Antenna Days of the Indian Ocean (IEEE RADIO 2015), September 21-24, 2015, Belle Mare, **MAURITIUS**
3. 11th IEEE International Conference on Semiconductor Electronics (IEEE ICSE 2014), August 27-29, 2014, Kuala Lumpur, **MALAYSIA**
4. International Training Workshop on FPGA Design for Scientific Instrumentation and Computing, November 11-22, 2013 at International Center for Theoretical Physics (ICTP), Trieste, **ITALY**.
5. 2nd International Conference on Engineering Mathematics and Physics' (ICEMP 2013), June 15-16, 2013, Colombo, **SRI LANKA**
6. 4th International Conference on Computer Modeling and Simulation (ICCMS 2013), February 24-25, 2013, Rome, **ITALY**
7. 15th International Conference of International Academy of Physical Sciences (CONIAPS XV), December 9-13, 2012, Rajamangala University of Technology, Thanyaburi, **THAILAND**
8. 10th IEEE International Conference on Semiconductor Electronics (IEEE ICSE 2012), September 19-21, 2012, Kuala Lumpur, **MALAYSIA**

MEMBERSHIP OF ACADEMIC BODIES

1. Life-Member, **ISCA** *(The Indian Science Congress Association, Kolkata, India),* **Membership No.-L-28293**
2. Member, **IEEE** *(Institute of Electrical and Electronics Engineers, **USA**),* **Membership No.-93575355**
3. Senior-Member, **IACSIT** *(International Association of Computer Science and Information Technology, **Singapore**)*, **Membership No.-80345831**
4. Senior-Member, **SCIEI** *(Science and Engineering Institute, **Hong Kong**, SAR of China,* **Membership No.-2013073001**
5. Life-Member, **NASI** *(The National Academy of Sciences, India, Allahabad, India)*
6. Life-Member, **IETE** *(The Institution of Electronics and Telecommunication Engineers, New Delhi, India),* **Membership No.-M-203187**
7. Life-Member, **IAPS** *(International Academy of Physical Sciences, Allahabad, India),* **Membership No.-N-12205**

EDITORIAL / REVIEWER BOARD MEMBERSHIP

1. Member, Editorial-Board, **IJAREEIE** *(International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering,* A Monthly peer reviewed journal, ISSN-online 2278-8875, ISSN-print 2320-3765*)*
2. Member, Editorial-Board, **JUSPS** *(Journal of Ultra Scientist of Physical Sciences,* International peer reviewed journal, ISSN-online 2319-8052, ISSN-print 2231-3478*)*
3. Member, Editorial-Board, **IRJ** *(International Researcher's Journal,* A Quarterly referred Journal, ISSN 2321-6301*)*
4. Member, Editorial-Board, **APM** *(Applied Physics and Mathematics)*, A Monthly peer reviewed International journal, ISSN-online 2333-4886, ISSN-print 2333-4878*)*
5. Member, Reviewer-Board, **IRJECE** *(International Journal of Electronics & Communication Engineering)*, A Monthly peer reviewed International journal, ISSN- 2395-0587*)*

INVITED LECTURES

1. **Invited Lecture** on *"Design and Analysis of Small Signal Amplifiers using Sziklai pairs as Active Components"* (July 16, 2017), 20th International Conference of International Academy of Physical Sciences on 'Recent Advances in Physical Sciences and Future Challenges' (**CONIAPS XX)**, Osmania University, Hyderabad, July 14-16, 2017
2. **Invited Lecture** on *"Sziklai pairs in Small-signal Amplifiers"* (March 26, 2017), Short Term Course on 'Emerging Trends in Material Science' (**STCETMS-2017)**, Madan Mohan Malaviya University of Technology, Gorakhpur, March 23-29, 2017

3. **Invited Lecture** on *"Foundation of Physics"*, J.B. Academy (A prestigious CBSC school), Faizabad August 19, 2017
4. **Invited Lecture** on *"Optical Computers"*, Physics Department, M.L.K. P.G. College, Balrampur October 07, 2016
5. **Invited Lecture** on *"Sziklai pairs in Small-signal Amplifiers"*(Sept. 26, 2014), National Conference on Integrated Circuits and Signal Processing (**ICSP-2014)**, Amity University, Lucknow Campus, Lucknow, September 25-26, 2014
6. **Invited Lecture** on "*Successful Research Approach Requires Positive Attitude*" in 'UGC teacher's training program' of Academic Staff College, D.D.U. Gorakhpur University, Gorakhpur, U.P. (September 28, 2013)

CONFERENCE/SEMINAR - ASSIGNMENTS

1. **Conference Co-Chair** in First International Conference on Computational and Mathematical Methods in Engineering & Technology (iCOMET'17), December 18-20, 2017, **Melbourne, Australia**
2. **Session Chair** (Sept.26) in National Conference on Integrated Circuits and Signal Processing (**ICSP-2014)**, Amity University, Lucknow Campus, Lucknow, September 25-26, 2014
3. **Session Chair** (March 22) in XVI Annual Conference of International Academy of Physical Sciences **(CONIAPS 2014)**, PDPM IIIT, Jabalpur, M.P., March 20-22, 2014
4. **Organizing Secretary**, National Seminar on Present Scenario of Higher Education in India: Prospects & Challenges, Dr. Ram Manohar Lohia Avadh University, Faizabad, September 19-20, 2015
5. **Organizing Secretary**, National Seminar on Emerging Trends in Electronics & Computer, March 11-12 1999 at Department of Physics & Electronics, Dr. R.M.L. Avadh University, Faizabad
6. Member, **Conference-Technical-Committee (MCCS-2015)**, 1st International Conference on Micro-electronics, Computing & Communication Systems, at ARTTC BSNL, Ranchi, November 14-15, 2015
7. Member, **Conference-Technical-Committee (NCCS-2015)**, 1st International Conference on Nano-electronics, Circuits & Communication Systems, at ARTTC, BSNL, Ranchi, April 25-26, 2015
8. Member, **National Advisory Committee** (**ICSP-2014)**, National Conference on Integrated Circuits and Signal Processing, Amity University, Lucknow Campus, Lucknow, September 25-26, 2014
9. Member, **Conference-Technical-Committee**, (**ICCSS-2014)**, International Conference on Circuits, System and Simulation, August 17-18, 2014, **Nottingham, UK**
10. Member**, Organizing Committee,** National Seminar on Materials and Its Applications; February 27-28, 2003, Dr. R.M.L. Avadh University, Faizabad

11. Member**, Organizing Committee,** National Seminar on Higher Education in India : Vision and Action; February 7, 2000 at Dr. R.M.L. Avadh University, Faizabad
12. **Coordinator,** Workshop on 'Computer Operation and Software Applications', Dr. Ram Manohar Lohia Avadh University, Faizabad, 9-15 March 1995

ADMINISTRATIVE EXPERIENCE

1. **Coordinator, RUSA Cell,** Dr. Ram Manohar Lohia Avadh University, Faizabad (April 6, 2017 till date)
2. **Coordinator, Department of Mass Communication & Journalism,** Dr. Ram Manohar Lohia Avadh University, Faizabad (April 06, 2017 till date)
3. **Media-in-charge,** Dr. Ram Manohar Lohia Avadh University, Faizabad (July 2003 till date)
4. **In-charge, Engineering Section,** Dr. Ram Manohar Lohia Avadh University, Faizabad (May 2016 till date)
5. **Member, Purchase Committee,** Dr. Ram Manohar Lohia Avadh University, Faizabad (April 26, 2017 till date)
6. **Member, IQAC,** (Internal Quality Assurance Cell) Dr. Ram Manohar Lohia Avadh University, Faizabad (April 10, 2015 till date)
7. **Head, Department of Physics and Electronics,** Dr. Ram Manohar Lohia Avadh University, Faizabad (July 19, 2014 to July 19, 2017)
8. **Coordinator UGC-Cell,** Dr. Ram Manohar Lohia Avadh University, Faizabad (March 13, 2015 to March 28, 2017)
9. **President, Sports Council,** Dr. Ram Manohar Lohia Avadh University, Faizabad (August 14, 2010 to August 14, 2012)
10. **Member, Executive-Council,** Dr. Ram Manohar Lohia Avadh University, Faizabad (June 22, 2012 to June 21, 2013 and October 10, 2001 to October 9, 2002)
11. **Assistant Dean Student Welfare (ADSW),** Dr. Ram Manohar Lohia Avadh University, Faizabad (September 1998 to December 2002)
12. **Coordinator, B.Tech Programs,** Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Faizabad (April 7 to Sept 30, 2001)
13. **Invited Member, NSS Advisory Board,** Dr. Ram Manohar Lohia Avadh University, Faizabad (Sessions 2011-12, 2012-13)

MEMBERSHIP OF ACADEMIC COMMITTEES

1. Member, **Academic-Council,** Dr. Ram Manohar Lohia Avadh University, Faizabad (July 2002 to June 2005 and July 19, 2014 till date)
2. Convener, **Research Development Committee (RDC)** of Physics & Electronics**,** Dr. Ram Manohar Lohia Avadh University, Faizabad (July 19, 2014 to July 19, 2017)

3. Convener **Board of Studies (BOS)** of M.Sc-Physics & M.Sc.-Electronics**,** Dr. Ram Manohar Lohia Avadh University, Faizabad (July 19, 2014 till date) and **Member BOS** during July 2000 to June 2002 and July 2009 to June 2011.
4. Member, **Faculty Board (Science Faculty),** Dr. Ram Manohar Lohia Avadh University, Faizabad (July 19, 2014 till date)
5. Convener, **Departmental Committee,** Department of Physics & Electronics Dr. Ram Manohar Lohia Avadh University, Faizabad (July 19, 2014 to July 19, 2017).
6. Member, **Board of Studies (BOS)** of B.Sc.-Electronics**,** Siddarth University, Kapilvastu, Siddarth Nagar (February 02, 2016 till date)

ACADEMIC QUALIFICATION

1. High School (1981) : Science group : **First** division with 71.7%
2. Intermediate (1983) : P.C.M. group : **First** division with 61.2%
3. B.Sc. (1986) : P.C.M. group : **First** division with 64%
4. M.Sc. (1988) : Physics (Electronics) : **First** division with 73.7%
5. P.G. Diploma (1990) : Computer Applications: with Grade-A
6. Ph.D. in Physics (1992) : **TITLE:** Investigations in Electroluminescence and Related Effects of II-VI Group Compounds
7. D.Sc. in Physics(Electronics): Registered in August 2017
   TITLE: Sziklai Pairs in Linear Electronic Circuits

ACADEMIC DISTINCTION

1. **Adarsh Vidya Saraswati Rastriya Puraskar** 2017 by Glacier Journal Research Foundation, Ahmedabad
2. Certificate of Excellence in Research by Education ExpoTv
3. One Research papers has been mentioned in May 30, 2012 issue of **Electronic Business Journal (USA's research news bulletin)**.
4. Highest Marks in M.Sc.
5. Highest Marks in Physics in B.Sc.

SOCIAL DISTINCTION

1. Member, Research Development Committee, Ayodhya Research Institute (An Autonomous Research Center of Ministry of Culture, UP Government)
2. Member, Rotary Club (An International Social Service Platform), Faizabad
3. Member, Management Board, Maharaja Public School, Ayodhya, Faizabad (an Intermediate level CBSE based public school)
4. Member, School Advisory Board, Navodaya Vidalaya, Faizabad (an Intermediate level CBSE based school of Government of India)

5. Member, School Management Committee, Jingle Bell Academy, Faizabad (an Intermediate level CBSE based leading public school of Faizabad)
6. Vice President, District Badminton Association, Faizabad

TEACHING EXPERIENCE

1. **24^{+} Years** teaching experience at PG level (M.Sc-Physics & M.Sc.-Electronics students) with following designation/responsibility and having Teaching expertise on the topics of Microprocessors, Communication Theories and Systems, Network Analysis, Digital Electronics and Linear Electronics

(a) Professor (AGP 10000)	01-01-2009 till Date
(b) Associate Professor (AGP 9000)	01-01-2006 to 01-01-2009
(c) Reader (3700-5700)	25-09-2001 to 01-01-2006
(d) Senior Lecturer (3000-5000)	25-09-1997 to 25-09-2001
(e) Lecturer (2200-4000)	25-09-1992 to 25-09-1997

RESEARCH EXPERIENCE

1. **26 Years** research experience (About **14 Years** research experience in the field of Electroluminescence and about **10 Years** research experience in the field of Circuit Simulation and Analysis).

   (a) **Supervised 15 Ph.D. Scholars** (As Supervisor - 13, As Co-supervisor - 02)

   (i) Ph.D. Awarded (Research Scholar / Topic / Degree Awarding Date)

   1. **Dinesh Singh** (Physics): Development of Red Light Emitting Electroluminophors and Their Characteristics (25-01-2002)
   2. **Raghavendra Pratap Singh** (Physics): Computation of Solar Constants for Prediction of Solar Radiations (12-12-2003)
   3. **Prem Kumar Singh** (Physics): Development of Binary Electroluminophors of II-VI Group Compounds and Their characteristics (01-08-2009)
   4. **Kranti Kirnesh Dubey** (Electronics): Development, Study and Simulation of Electronic Circuits, Designed By Coupling of Active Electronic Components (17-01-2009)
   5. **Satendra Nath Tiwari** (Physics): Development of Linear Electronic Circuits By Coupling of Various Junction Devices and Their Analysis Through Simulation Software (28-02-2009)
   6. **Geetika Srivastava** (Electronics): Development and Modification of Wave-shaping Circuits through PSpice and their Physical Verification (16-09-2009)
   7. **Arvind Kumar Dewedi** (Physics)**:** Development and Simulation Studies of Electronic Circuits, Configured by Coupling of Diodes and Transistors (16-05-2010)
   8. **Jitendra Singh** (Physics)**:** Development, Modification and Analysis of Diode and BJT Based Linear Circuits Through PSPice (24-12-2013)

9. **Beena Pandey** (Physics)**:** Development and Analysis of Modified Small Signal Amplifiers Through PSpice (28-06-2014)
10. **Meena Singh** (Electronics): Floating Admittance Technique for Programming Functions of Amplifiers (01-08-2014)
11. **Susmrita Srivastava** (Electronics): Development and Analysis of Modified JFET and MOSFET Amplifiers Through PSpice (23-09-2014)
12. **Sashwati Manohar** (Physics): Electro–optical and Electrical Properties of Mesogenic Materials and their Polymer Composites (07-02-2015)
13. **Ramendra Singh** (Electronics): Development, Modification and Analysis of Small Signal BJT Amplifiers Through PSpice (16-08-2016)

(ii) Ph.D. Registration for Co-supervision (Scholar's Name / Topic / Registration Year)

14. **Shipra Saraswat** (Electronics): Development of Real Time System for Efficient Processing of ECG Data Using Techniques of Artificial Intelligence (March 8, 2013)
15. **Santosh Kumar Gupta** (Electronics): Comparative Analysis and Development of a CFO Removal Scheme in OFDM System (April 8, 2013)

EXAMINATION RELATED RESPONSIBILITIES

1. **State Coordinator**, 'Combined Pre-Medical Test (CPMT-2016) (Conducting body - Dr. Ram Manohar Lohia Avadh University, Faizabad, U.P.)
2. **State Deputy Convener**, 'Common Eligibility Test Ph.D.-2012' (Conducting body - Dr. Ram Manohar Lohia Avadh University, Faizabad, U.P.)
3. **District Coordinator,** State level B.Ed. Entrance Examination 2007 (organized by C S J M Kanpur University, U.P.)
4. **Coordinator**, 'Common Eligibility Test for Ph.D.-2016 (PhD Entrance Test-2016)' of Dr. Ram Manohar Lohia Avadh University, Faizabad, U.P.
5. **Deputy District Coordinator,** State level B.Ed. Entrance Examinations 2008, 2009 and 2010
6. **Coordinator,** M.Ed. Entrance Examination-2011 of Dr. Ram Manohar Lohia Avadh University, Faizabad, U.P.
7. **Deputy Coordinator,** B.Ed. Entrance Examination 2005 and 2006 of the University.
8. **Deputy Coordinator,** M.Ed. Entrance Examination 2006 and 2007 of the University
9. **Center Superintendent,** University (Campus) Examinations in 2001-02, 2005-06, 2006-07 and 2007-08 sessions
10. **Center Superintendent** (Campus Center), B.Ed., M.Ed., B.PEd. and Law Entrance Examination of the University in 2004-05 and 2006-07
11. **Center Superintendent, CPMT**-2001, 2003, 2006 and 2007 at Dr. R.M.L.Avadh University, Faizabad Center.

12. **Center Observer**, M.Ed.-Entrance-2014, B.Ed.-Entrance-2011, CPMT-2015, B.Ed.-Entrance-2017 at different Examination Centers of Faizabad

CO-CURRICULAR AND OTHER ACTIVITIES

1. Conducted (Anchored) 16th Convocation program of Narendra Dev University of Agriculture and Technology, Kumarganj, Faizabad on special invitation of the varsity's Vice chancellor
2. **Chief Editor** of SOUVENIRS / University Annual Magazines published on the occasion of 13th 14th 15th 18th and 19th convocations of Dr. R.M.L. Avadh University, Faizabad
3. Approved **Counselor of IGNOU** Indira Gandhi National Open University (Counselor Code-093007) for CIC course (Since 2001)
4. **Convener,** North Zone Inter-varsity Kho-Kho (M/F) and North Zone Inter-varsity Cricket (F) tournaments during 2011-12
5. **Convener,** District level '**Science Awareness Year–2004**' programs
6. **Co-convener,** Inter Collegiate Youth Festivals (16,17 January 1999; 2,3 November 1999; 21,22 December 2001 and 21,22 November 2002)
7. Member, Selection Committees (more than two dozen) of various Degree Colleges and Inter Colleges.
8. Presented more than a dozen of **Radio-talks** at **Akashwani, Faizabad**
9. Delivered special lecture in 'Teacher's training program' of Navodaya Vidyalaya Samiti, Navodaya Vidyalaya, Faizabad, U.P. (July 21, 2014)

REFRESHER COURSE / TRAINING PROGRAM

1. **Six Days Training Program** on Computer Interfaced Science Experiments, Inter-University Accelerator Center (IUAC), New Delhi, April 29 to May 4, 2013
2. **Refresher Course** in Physics, July 24 to August 13, 2001 (21 days), University of Lucknow, Lucknow.
3. **Refresher Course** on "Experimental Physics"; April 10 to May 9, 2001 (30 days), Aligarh Muslim University, Aligarh
4. **One-day Training Program** on 'NICNET Communication Systems', NIC, Lucknow, 30-11-1997
5. **Refresher Course** on "Recent Advances in General and Material Science, Microelectronics and Computer Applications"; August, 12 to September, 2, 1996 (21 days), A.P.S. University, Rewa (M.P.)

INTERNATIONAL CONFERENCE/WORKSHOP

1. **IEEE International Conference** on Advances in Electrical, Electronics & System Engineering (IEEE-ICAEESE 2016), November 14-16, 2016, Kuala Lumpur, **MALAYSIA** (**Presented Paper –** New Small-signal Amplifying System with Sziklai pairs in Triple-Transistor Topology

2. 18th **International Conference** of International Academy of Physical Sciences (CONIAPS XVIII) on Recent Trends in Physical Sciences, December 22-24, 2015 at University of Allahabad, Allahabad, U.P., India (**Presented Paper -** Small-signal Amplifiers with PNP and NPN Sziklai Pairs)
3. **IEEE Radio and Antenna Days of the Indian Ocean** (IEEE RADIO 2015), September 21-24, 2015, Belle Mare, **MAURITIUS** (**Presented Paper -** Novel Circuit of Small-signal Amplifier with BJT-MOSFET Hybrid Unit in Sziklai Pair Topology)
4. 17th **International Conference** of International Academy of Physical Sciences (CONIAPS XVII) on Emerging Trends in Physical Sciences & Technology, January 16-18, 2015 at University of Rajasthan, Jaipur (**Presented Paper -** Small-signal FET Amplifiers with Triple Darlington Topology)
5. 11th **IEEE International Conference** on Semiconductor Electronics (IEEE-ICSE 2014), August 27-29, 2014, Kuala Lumpur, **MALAYSIA** (**Presented Paper -** Two-stage Small-signal Amplifier with Darlington and Sziklai pairs
6. XVI **International Conference** of International Academy of Physical Sciences (CONIAPS 2014), March 20-22, 2014 at PDPM IIIT, Jabalpur, M.P. (**Presented Paper -** Small-signal Amplifiers with Hybrid Combination of Three Unlike Active Devices in Triple Darlington Topology)
7. **International Training Workshop** on FPGA Design for Scientific Instrumentation and Computing' November 11 to 22, 2013 at International Center for Theoretical Physics (ICTP), Trieste, **ITALY**.
8. 2nd **International Conference** on Engineering Mathematics and Physics (ICEMP 2013), June 15-16, 2013, Colombo, **SRI LANKA** (**Presented Paper -** A New Circuit Model of Small-signal Sziklai pair amplifier)
9. 4th **International Conference** on Computer Modeling and Simulation (ICCMS 2013), February 24-25, 2013, Rome, **ITALY** (**Presented Paper -** Development and Qualitative Analysis of a New Circuit Model of Two-stage Small-Signal Sziklai pair Amplifier)
10. 15th **International Conference** of International Academy of Physical Sciences (CONIAPS XV), December 9-13, 2012, Thanyaburi, **THAILAND** (**Presented Papers -** A Small-signal Amplifier Developed by using Three Dissimilar Active Devices in Triple Darlington Topology **and** New Circuit Model of Small-signal Amplifier Developed by using MOSFETs in Triple Darlington Configuration)
11. 10th **IEEE International Conference** on Semiconductor Electronics (IEEE-ICSE 2012), September 19-21, 2012, Kuala Lumpur, **MALAYSIA** (**Presented Papers -** Qualitative Study of a New Circuit Model of Small-signal Amplifier using Sziklai pair in Compound Configuration **and** New Circuit Models of Complementary-Symmetry Class-AB and Class-B Push-Pull Amplifiers)
12. **International Workshop** on 'The Use of Computers in Teaching Physics', R.D. University, Jabalpur, M.P., December 3 to 9, 1992

NATIONAL CONFERENCES / SEMINARS / WORKSHOPS

1. National Conference on Functional Materials, Department of Physics, Deen Dayal Upadhyaya Gorakhpur University, Gorakhpur, March 10-12, 2016 (**Presented Paper-** Small-Signal Amplifying System with Three Dissimilar Active Devices)
2. National Seminar on Present Scenario of Higher Education in India: Prospects & Challenges, Dr. Ram Manohar Lohia Avadh University, Faizabad, September 19-20, 2015 (**Presented Paper-** Importance and Necessity of ICT in Higher Education)
3. National Seminar on Current Trends in Mathematics with Special Focus on Operation Research and Computers, Department of Mathematics and Statistics, Dr. R.M.L. Avadh University, Faizabad, March 28-29, 2010 (**Presented Paper-** Qualitative Analysis of Small Signal Wide Band Triple Darlington Amplifier)
4. National Symposium on Advances in Material Science, Department of Physics, D.D.U. University, Gorakhpur, March 17-19, 2005 (**Presented Paper-** On the Magneto-optical Effect of Modulated Diode Lasers)
5. Spice Models for Advanced VLSI Circuit Simulations, Department of Electronic Science, University of Delhi (South Campus) 11-12 December 2005 (**Presented Paper-** Logic Gates, Based on Series and Parallel Combination of Like Diodes)
6. National Seminar on Materials and Its Applications; February 27-28, 2003, Dr. R.M.L. Avadh University, Faizabad (**Presented Papers**- Comparison of Electroluminescence in ZnO-CdO and ZnO-CdS Base Binary Systems)
7. National Seminar on Higher Education in India : Vision and Action; February 7, 2000 at Dr. R.M.L. Avadh University, Faizabad
8. National Seminar on Emerging Trends in Electronics & Computer; March 11,12 1999, Dr. R.M.L. Avadh University, Faizabad (**Presented Papers-** Photoluminescence in Double Band Emitting (ZnS+HgO):Cu,Mn(H) Binary Systems **and** Electroluminescent Behaviour of Red Light Emitting ZnSe:Mn Phosphors)
9. Second Conference of the 'International Academy of Physical Sciences', Guru Ghasidas University, Bilaspur, 13-14 December 1997 (**Presented Paper-** Electroluminescent Studies of ZnO Base Electroluminophors **and** Electroluminescence in ZnO-CdO and ZnO-CdS Base Binary System)
10. Workshop on 'Computer Operation and Software Applications', Dr. Ram Manohar Lohia Avadh University, Faizabad, 9-15 March 1995 (**As Coordinator**) (**Presented Talk**- Computer and Its internal Organization)
11. National Seminar on CAD/CAM, KNIT and HAL, Sultanpur, 27-29 January 1993
12. Diamond Jubilee Session of 'The National Academy of Sciences', Allahabad University, 3-5 May 1991 (**Presented Paper-** Electroluminescent Brightness Wave Forms in Triple Band Electroluminophors ZnS:Cu,Mn(H) **and** Photoluminescence of Doubly Activated Zinc Sulphide Phosphors)

PUBLISHED BOOKS

1. Small Signal Amplifiers with JFETs and MOSFETs in Darlington's Topology, Published By- LAMBERT Academic Publishing, **GERMANY** (ISBN – 978-3-659-62783-5)
2. Digital Communication**,** Shree Publishers & Distributers, ISBN-978-81-8329-807-0

PUBLISHED RESEARCH PAPERS (79)

A. PAPERS PUBLISHED IN JOURNALS (63)

1. *Classification of ECG Signals using Cross Recurrence Quantification Analysis and Probabilistic Neural Network Classifier for Ventricular Tachycardia Patients,* S. Saraswat, G. Srivastava, S.N. Shukla, International Journal of Biomedical Engineering and Technology, ISSN No: 1752-6426, (**Under Publication**: Abstract is available on Journal's website as paper under publication)
2. *Novel Design of 10T Full Adder with 180nm CMOS Technology*, K.K. Verma, **S.N. Shukla,** S.K. Jaiswal and Maharishi Vaish, International Journal of Electronics Engineering Research, Vol.9, Number 9, **ISSN-0975-6450,** pp.1407-1414, 2017
3. *Design and Analysis of Phase Frequency Detector using D Flip-Flop for PLL Application*, **S.N. Shukla,** K.K. Verma, S.K. Jaiswal and S. Chaurasiya, International Journal of Electronics Engineering Research, Vol.9, Number 9, **ISSN-0975-6450,** pp.1389-1395, 2017
4. *Malignant Ventricular Ectopy Classification using Wavelet Transformation and Probabilistic Neural Network Classifier*, S. Saraswat, G. Srivastava, **S.N. Shukla**, Indian Journal of Science & Technology, Vol.9 (40), DOI: 10.17485/ijst/2016/v9i40/95486, **ISSN-0974-6846,** pp.1-5, October 2016 **(IF-0.68)**
5. *Development of Small-Signal Amplifier using BJT-JFET Hybrid Unit in Sziklai Pair Topology*, **S.N. Shukla**, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.4, Issue 12, DOI:10.15662/IJAREEIE.2015.0412021, **ISSN-2278-8875,** pp.9818-9822, 2015 **(IF-5.016)**
6. *An Experimental Study of the Common Emitter Amplifier using Differential Amplifiers*, R. Singh, **S. N. Shukla,** Pragyaan: Journal of Information Technology, Vol.13, Issue 1, **ISSN-0974-5513,** pp.14-16, 2015
7. *Development of Wave Shaping Circuits through PSpice and their Physical Verification*, R. Singh, **S. N. Shukla,** Pragyaan: Journal of Information Technology, Vol.13, Issue 1, **ISSN-0974-5513,** pp.01-03, 2015
8. *Qualitative Analysis of Darlington pair Based Modified Small-signal amplifier*, **S. N. Shukla,** R. Singh, B. Pandey, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.4, Issue 4, DOI:10.15662/ijareeie.2015.0404078, **ISSN-2278-8875,** pp.2181-2186, 2015 **(IF-5.016)**
9. *Small-Signal Amplifiers with BJT, FET and MOSFET in Triple Darlington Topology*, **S. N. Shukla**, N. K. Chaudhary, International Journal of Engineering and Advanced Technology, **ISSN-2249-8958**, Vol.4 No.1, pp.102-106, 2014 **(IF-1.097)**

10. *Small-signal amplifier with JFETs in triple Darlington topology*, **S. N. Shukla,** International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.3, Issue 9, **ISSN-2278-8875,** p-18812, September 2014 **(IF-3.738)**
11. *A New Circuit Model of Small-signal Sziklai pair amplifier*, **S.N.Shukla** and S.Srivastrava, International Journal of Applied Physics and Mathematics, Vol.3, No.4, DOI: 10.7763/IJAPM.2013.V3.211, **ISSN-2010-363X,** pp.231-236, 2013
12. *A New Circuit Model of Small-signal Amplifier using JFETs in Darlington pair Configuration*, S.N.Shukla and S.Srivastrava, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.2, Issue 4, **ISSN-2278-8875,** p-1554-1560, April 2013 **(IF-1.686)**
13. *Development and Qualitative Analysis of a New Circuit Model of Two-stage Small-signal Sziklai pair Amplifier*, **S.N.Shukla** and S.Srivastrava, International Journal of Computer Theory and Engineering, Vol.5, No. 4, DOI: 10.7763/IJCTE.2013.V5.772, **ISSN-1793-8201,** pp.668-672, 2013
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